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GIOIOSA, ROBERTO ; SANCHO, JOSE ; PETRINI, FABRIZIO ; DAVIS, KEI
LA-CC-04-132 ; 20-oct-2004

2.

PERS FROM PROJECTIONS FOR BINDING-TIME ANALYSIS.

DAVIS, KEI
LA-UR-95-3632 ; 13-oct-1995

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3.

EXPLOITING TEMPORAL LOCALITY IN STENCIL BASED APPLICATIONS.
BASSETTI, FEDERICO ; DAVIS, KEI
LA-UR-99-3390 ; 25-jun-1999

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4.

Optimization Principles for Codesign Applied to Molecular Dynamics
Davis, Kei
LA-UR-12-20664 ; 2012-04-18

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5.

Optimization Principles for Computational Co-design with Applications to Molecular Dynamics
Davis, Kei
LA-UR-12-20661 ; 2012-04-18

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6.

Optimization Principles for Codesign Applied to Molecular Dynamics
Davis, Kei
LA-UR-12-20654 ; 2012-04-17

[Full Text](#)

7.

APPROACH TO EXTREME-SCALE SIMULATION OF NOVEL ARCHITECTURES.
ALEXANDER, F. J. ; BERKBIGLER, KATHRYN P. ; BOOKER, GRAHAM ; BUSH, BRIAN W. ;
DAVIS, KEI ; et al.
LA-UR-01-4087 ; 23-jul-2001

[Full Text](#)

8.

**GRAPH VISUALIZATION FOR THE ANALYSIS OF THE STRUCTURE AND DYNAMICS OF
SCALE SUPERCOMPUTERS.**
BERKBIGLER, KATHRYN P. ; BUSH, BRIAN W. ; DAVIS, KEI ; HOISIE, ADOLFY ; SMITH, STEVE
A.
LA-UR-02-1929 ; 03-apr-2002

Full Text

9.

ARCHITECTURAL SUPPORT FOR SYSTEM SOFTWARE ON LARGE-SCALE CLUSTERS.
DAVIS, KEI ; FRACHTENBERG, EITAN ; PETRINI, FABRIZIO ; JOSE, SANCHO C
LA-UR-04-3795 ; 03-jun-2004

Full Text

10.

DESIGNING PARALLEL OPERATING SYSTEMS VIA PARALLEL PROGRAMMING.
DAVIS, KEI ; FRACHTENBERG, EITAN ; PETRINI, FABRIZIO ; SANCHO, JOSE
LA-UR-04-3796 ; 03-jun-2004

11.

CURRENT PRACTICE AND A DIRECTION FORWARD IN CHECKPOINT/RESTART IMPLEMENTATIONS FOR FAULT TOLERANCE.
SANCHO, JOSE C. ; PETRINI, FABRIZIO ; DAVIS, KEI ; GIOIOSA, ROBERTO
LA-UR-05-0146 ; 11-jan-2005

12.

PERFORMANCE ANALYSIS: MEASUREMENT, MODELING AND MORE!
KERBYSON, DARREN J. ; BARKER, KEVIN J. ; DAVIS, KEI ; FRACHTENBERG, EITAN ; JIANG, SONG ; et al.
LA-UR-05-3777 ; 19-may-2005

13.

TOWARDS HIGHLY EFFICIENT, SCALABLE AND TRANSPARENT FAULT TOLERANCE FOR EXTREME- SCALE PARALLEL COMPUTERS
DAVIS, KEI ; JIANG, SONG ; SANCHO, JOSE
LA-UR-05-8172 ; 21-oct-2005

14.

COORDINATED MULTI-LEVEL BUFFER CACHE MANAGEMENT WITH CONSISTENT ACCESS LOCALITY QUANTIFICATION
JIANG, SONG ; DAVIS, KEI ; ZHANG, XIAODONG
LA-UR-05-8790 ; 16-nov-2005

15.

TRANSPARENT, INCREMENTAL CHECKPOINTING AT KERNEL LEVEL:A FOUNDATION FOR FAULT TOLERANCE FOR PARALLEL COMPUTERS
SANCHO, JOSE C. ; GIOIOSA, ROBERTO ; JIANG, SONG ; PETRINI, FABRIZIO ; DAVIS, KEI
LA-UR-05-8791 ; 16-nov-2005

16.

QUANTIFYING THE POTENTIAL BENEFIT OF OVERLAP IN LARGE-SCALE SCIENTIFIC COMPUTING
SANCHO, JOSE CARLOS ; BARKER, KEVIN J. ; KERBYSON, DARREN J. ; DAVIS, KEI
LA-UR-06-3109 ; 27-apr-2006

17.

DISKSEEN: A PREFETCH POLICY BASED ON DISK LAYOUT AND ACCESS HISTORY
JIANG, SONG ; DAVIS, KEI ; DING, XIAONING ; FENG, CHEN ; ZHANG, XIODONG
LA-UR-06-3280 ; 04-may-2006

18.

ANALYZING THE POTENTIAL OVERLAP BETWEEN COMMUNICATION AND COMPUTATION
SANCHO, JOSE ; BARKER, KEVIN J. ; KERBYSON, DARREN J. ; DAVIS, KEI
LA-UR-06-8075 ; 14-nov-2006

19.

QUANTIFYING THE POTENTIAL BENEFIT OF OVERLAPPING COMMUNICATION AND COMPUTATION IN LARGE-SCALE SCIENTIFIC APPLICATIONS

SANCHO, JOSE ; BARKER, KEVIN J. ; KERBYSON, DARREN J. ; DAVIS, KEI

LA-UR-06-8076 ; 14-nov-2006

20.

HIDING COMMUNICATION OVERHEADS IN HIGH PERFORMANCE COMPUTING

SANCHO PITARCH, JOSE CARLOS ; BARKER, KEVIN ; KERBYSON, DARREN ; KEI, DAVIS

LA-UR-07-1041 ; 15-feb-2007

21.

DISKSEEN: A PREFETCH POLICY BASED ON DISK LAYOUT AND ACCESS HISTORY

DAVIS, MARION KEI ; DING, XIAONING ; JIANG, SONG ; CHEN, FENG ; ZHANG, XIAODONG

LA-UR-07-3056 ; 07-may-2007

22.

ANALYSIS OF THE WEATHER RESEARCH AND FORECASTING (WRF) MODEL ON LARGE-SCALE SYSTEMS

KERBYSON, DARREN J. ; BARKER, KEVIN J. ; DAVIS, MARION KEI

LA-UR-07-6376 ; 19-sep-2007

Full Text

23.

COMPARISON OF PERFORMANCE-ENHANCING STRATEGIES FOR PARALLEL NUMERICAL OBJECT-ORIENTED FRAMEWORKS.

BASSETTI, FEDERICO ; DAVIS, KEI ; QUINLAN, DANIEL

LA-UR-97-3888 ; 25-sep-1997

Full Text

24.

SCRIPTING LANGUAGE DESIGN FOR LARGE-SCALE OBJECT-ORIENTED COMPONENT-BASED APPLICATION FRAMEWORKS.

DAVIS, KEI

LA-UR-99-3394 ; 25-jun-1999

Full Text

25.

OPTIMIZING CODE TRANSFORMER FOR C++ OBJECT-ORIENTED ARRAY CLASS LIBRARIES.

DAVIS, KEI ; QUINLAN, DANIEL

LA-UR-99-3395 ; 25-jun-1999

26.

OPTIMIZING TRANSFORMATIONS OF STENCIL OPERATIONS FOR PARALLEL CACHE-BASED ARCHITECTURES.

BASSETTI, FEDERICO ; DAVIS, KEI

LA-UR-99-3396 ; 25-jun-1999

27.

Improving multilevel cache management via analysis of locality measure performance

Davis, Marion Kei

LALP-06-146 ; 2006

Full Text

28.

A Solid-state-drive-based Buffering Scheme for Reducing IO Collisions on Shared Hard-disk-based Storage Systems

Zhang, Xuechen ; Davis, Kei ; Jiang, Song

LA-UR-11-11376 ; 2011

29.

PAL ROADRUNNER REPORT #5: A NOTE ON APPLICATION PERFORMANCE OF THE

EDP VERSION OF THE CELL

KERBYSON, DARREN ; HOISIE, ADOLFY ; PAKIN, SCOTT ; DAVIS, MARION KEI ; LANG, MICHAEL ; et al.

LA-CP-07-1147 ; 26-sep-2007

30.

DESIGN AND IMPLEMENTATION OF LOW-AND MEDIUM-FIDELITY NETWORK SIMULAITONS OF A 30 TERAOPS SYSTEM.

ALEXANDER, F. J. ; BERKBIGLER, KATHRYN P. ; BOOKER, GRAHAM ; BUSH, BRIAN W. ; DAVIS, KEI ; et al.

LA-UR-02-1930 ; 03-apr-2002

Full Text

31.

DESIGN, IMPLEMENTATION, AND VALIDATION OF LOW- AND MEDIUM-FIDELITY NETWORK SIMULATIONS OF A 30- TERAOPS SYSTEMS.

ALEXANDER, F. J. ; BERKBIGLER, KATHRYN ; BOOKER, GRAHAM ; BUSH, BRIAN ; DAVIS, KEI ; et al.

LA-UR-02-6573 ; 16-oct-2002

32.

PAL roadrunner report #6: a first look at the performance of one Roadrunner CU

Barker, Kevin J ; Davis, Marion Kei ; Hoisie, Adolfy ; Kerbyson, Darren J ; Lang, Michael ; et al.

LA-CP-08-00476 ; 2008

33.

Performance modeling of Jaguar - pre and post upgrade

Barker, Kevin J ; Davis, Marion Kei ; Kerbyson, Darren J

LA-UR-08-00773 ; 2008

34.

Performance modeling of Jaguar - pre and post upgrade

Barker, Kevin J ; Davis, Marion Kei ; Kerbyson, Darren J

LA-UR-08-01997 ; 2008

35.

Roadrunner Technical Seminar Series: Overview of Modeling, Performance and Results

Barker, Kevin J ; Davis, Marion Kei ; Hoisie, Adolfy ; Lang, Michael ; Pakin, Scott ; et al.

LA-UR-08-02037 ; 2008

36.

Performance modeling in action: Performance prediction and optimization of jaguar system during upgrade.

Barker, Kevin J ; Davis, Marion Kei ; Kerbyson, Darren J

LA-UR-08-02553 ; 2008

Full Text

37.

Performance assessment of major tri-lab ASC platforms

Barker, Kevin J ; Davis, Kei ; Hoisie, Adolfy ; Kerbyson, Darren J ; Lang, Michael K ; et al.

LA-UR-09-05919 ; 2009

38.

QoS support for end users of I/O-intensive applications using shared storage systems

Davis, Marion Kei ; Zhang, Xuechen ; Jiang, Song

LA-UR-11-00409 ; 2011

Full Text

39.

Comparing current cluster, massively parallel, and accelerated systems

Barker, Kevin J ; Davis, Kei ; Hoisie, Adolfy ; Kerbyson, Darren J ; Pakin, Scott ; et al.

Full Text

40.

Orchestrator: improving the performance of multi-node I/O systems via inter-server coordination

Davis, Marion Kei ; Zhang, Xuechen ; Jiang, Song

LA-UR-10-02601 ; 2010

Full Text

41.

iHarmonizer: improving the disk efficiency of I/O-intensive multithreaded codes

Davis, Marion Kei ; Wang, Yizhe ; Jiang, Song

LA-UR-10-02602 ; 2010

Full Text

42.

iTransformer: Using SSD to Improve Disk Scheduling for Higher-performance I/O

Davis, Kei

LA-UR-11-11688 ; 2011

Full Text

43.

Modeling Data-Access Bandwidth in NUMA Architectures

Braithwaite, Ryan Karl ; Davis, Kei ; Feng, Wu ; McCormick, Patrick S.

LA-UR-12-10226 ; 2012

44.

iBridge: Improving Unaligned Parallel File Access with Solid-State Drives

Davis, Kei

LA-UR-12-21214 ; 2012-05-08

45.

Pal Roadrunner Report #7: Pre-delivery Performance Testing Of Roadrunner (17 Cus)

Barker, Kevin J ; Davis, Marion Kei ; Hoisie, Adolfo ; Kerbyson, Darren J ; Lang, Michael ; et al.

LA-CP-08-01083 ; 2008

46.

Performance analysis and modeling

Kerbyson, Darren J ; Barker, Kevin J ; Davis, Marion Kei ; Hoisie, Adolfo ; Lang, Michael ; et al.

LA-CP-08-00656 ; 2008

47.

An early performance evaluation of the Intel nehalem processor

Barker, Kevin J ; Davis, Marion Kei ; Hoisie, Adolfo ; Kerbyson, Darren J ; Lang, Mike ; et al.

LA-CP-08-00669 ; 2008

48.

Roadrunner performance and tools

Pakin, Scott ; Barker, Kevin J ; Davis, Marion Kei ; Hoisie, Adolfo ; Kerbyson, Darren J ; et al.

LA-UR-08-00979 ; 2008

49.

Advance HPC networks: performance analysis activities at LANL

Barker, Kevin J ; Davis, Marion Kei ; Kerbyson, Darren J ; Lang, Mike

LA-UR-08-01998 ; 2008

50.

Entering the petaflop era: the architecture and performance of roadrunner

Barker, Kevin J ; Davis, Marion Kei ; Hoisie, Adolfo ; Kerbyson, Darren J ; Lang, Michael ; et al.

LA-UR-08-02556 ; 2008

51.

An Early Performance Evaluation of the SiCortex SC648

Barker, Kevin, J. ; Davis, Kei ; Kerbyson, Darren, J. ; Lang, Mike ; Pakin, Scott ; et al.

LA-UR-08-02465 ; 2008

52.

An early performance evaluation of the SiCortex SC648

Kerbyson, Darren J ; Barker, Kevin J ; Davis, Marion Kei ; Lang, Michael ; Pakin, Scott ; et al.

LA-UR-08-02845 ; 2008

53.

Moore, more cores, and more application performance

Kerbyson, Darren J ; Barker, Kevin ; Davis, Marion Kei ; Hoisie, Adolfo ; Lang, Michael ; et al.

LA-UR-08-02847 ; 2008

54.

Experiences in scaling scientific applications on current-generation quad-core processors

Barker, Kevin J ; Davis, Marion Kei ; Hoisie, Adolfo ; Kerbyson, Darren J ; Lang, Mike ; et al.

LA-UR-08-02850 ; 2008

55.

Roadrunner performance acceptance testing

Barker, Kevin J ; Davis, Marion Kei ; Hoisie, Adolfo ; Kerbyson, Darren J ; Lang, Michael K ; et al.

LA-CP-09-00193 ; 2009

56.

Analysis of the weather research and forecasting (WRF) model on large-scale systems Cancelled

Kerbyson, Darren J ; Barker, Kevin J ; Davis, Kei

LA-UR-07-07184 ; 2007

Full Text

57.

Making resonance a common case: a high-performance implementation of collective I/O on parallel file systems

Davis, Marion Kei ; Zhang, Xuechen ; Jiang, Song

LA-UR-09-00201 ; 2009

58.

An early look at the performance of AMD MagnyCours

Kerbyson, Darren J ; Barker, Kevin J ; Davis, Marion Kei ; Hoisie, Adolfo ; Lang, Michael K ; et al.

LA-CP-09-01163 ; 2009

Full Text

59.

Performance analysis and modeling from design to installation of the peta-scale roadrunner system

Davis, Marion Kei ; Hoisie, Adolfo ; Kerbyson, Darren J ; Lang, Michael K ; Barker, Kevin J ; et al.

LA-UR-09-04768 ; 2009

Full Text

60.

Experiences from the Roadrunner petascale hybrid systems

Kerbyson, Darren J ; Pakin, Scott ; Lang, Mike ; Sancho Pitarch, Jose C ; Davis, Kei ; et al.

LA-UR-10-01119 ; 2010

61.

Algorithms for Optimizing the Eulerian Applications Code Base for Future Computational Architectures

Robey, Robert W. ; Nicholaeff, David ; Robey, Rachel N. ; McCormick, Patrick S. ; Davis, Marion K. ; et al.

LA-UR-12-22644 ; 2012-07-03

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